

SC090N75ZLT-R

Silicon Carbide MOSFET 750V, 90mΩ, 36A



重庆平伟半导体股份有限公司

Features

- 0 V turn-off gate voltage can be applied
- High Blocking Voltage with Low RDS(on)
- High Speed Switching with Low Capacitances
- Easy to Parallel and Simple to Drive
- Qualified according to JEDEC criteria



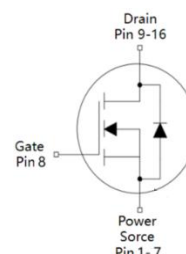
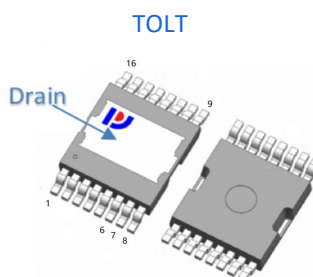
100% DVDS Tested
100% Avalanche Tested

Applications

- Lighting
- Telecom and UPS
- PWM stages for e.g. PC Silverbox, Adapter, LCD & PDP TV

Product Summary

V_{DS}	750V
$R_{DS(on)}$ typ.	90mΩ
I_D	36A



Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
SC090N75ZLT-R	SC090N75ZLT	TOLT	Tape&Reel	13 inches	24mm	1200pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	750	V
Continuous drain current $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	I_D	36 25	A
Pulsed drain current ($T_C = 25^\circ\text{C}$)	$I_{D \text{ pulse}}$	107	A
Avalanche energy, single pulse ($L=5\text{mH}$)	E_{AS}	63	mJ
Gate-Source voltage,max.transient voltage	V_{GSmax}	-10/+22	V
Recommended operating values	V_{GSop}	0/+18	V
Power dissipation $T_C = 25^\circ\text{C}$	P_{tot}	133	W
Operating junction and storage temperature	T_j, T_{stg}	-55...+175	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – case.	R_{thJC}	-	0.8	1.1	°C/W	-
Thermal resistance, junction - ambient(min. footprint)	R_{thJA}	-	-	40	°C/W	

Electrical Characteristic (at $T_j = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV_{DSS}	750	-	-	V	$V_{GS}=0V, I_D=500\mu A$
Gate threshold voltage	$V_{GS(th)}$	2.7	3.6	4.5	V	$V_{DS}=V_{GS}, I_D=7.5mA$
Zero gate voltage drain current	I_{DSS}	-	1	10	μA	$V_{DS}=750V, V_{GS}=0V$ $T_j=25^{\circ}C$ $T_j=175^{\circ}C$
Gate-source leakage current	I_{GSS}	-	-	100	nA	$V_{GS}=22V, V_{DS}=0V$
		-	-	-100	nA	$V_{GS}=-10V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	90	120	mΩ	$V_{GS}=15V, I_D=17A$
Drain-source on-state resistance	$R_{DS(on)}$	-	68	-	mΩ	$V_{GS}=18V, I_D=17A$
Transconductance	g_{fs}	-	10.0	-	S	$V_{DS}=20V, I_D=16A$

Dynamic Characteristic

Input Capacitance	C_{iss}	-	1063	-	pF	$V_{GS}=0V, V_{DS}=500V,$ $f=1MHz$
Output Capacitance	C_{oss}	-	81	-		
Reverse Transfer Capacitance	C_{rss}	-	4	-		
Gate Total Charge	Q_G	-	30	-	nC	$V_{DS}=500V, I_D=17A$ $V_{GS}=0V/15V$
Gate-Source charge	Q_{gs}	-	10	-		
Gate-Drain charge	Q_{gd}	-	13	-		
Turn-on delay time	$t_{d(on)}$	-	52	-	ns	$V_{GS}=0/15V, V_{DD}=500V,$ $R_G=20\Omega, I_D=17A$
Rise time	t_r	-	28	-		
Turn-off delay time	$t_{d(off)}$	-	50	-		
Fall time	t_f	-	33	-		
Gate resistance	R_G	-	0.7	-	Ω	$V_{GS}=0V, f=1MHz$

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Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	3.3	-	V	$V_{GS}=0V, I_{SD}=8.5A$ $T_j=25^{\circ}C$
		-	2.8	-		$T_j=175^{\circ}C$
Body Diode Continuous Forward Current	I_S	-	-	36	A	$T_C = 25^{\circ}C$
		-	-	28	A	$T_C = 100^{\circ}C$
Body Diode Reverse Recovery Time	t_{rr}	-	52	-	ns	$V_{GS}=-5V, I_{SD}=17A,$ $V_R=500V$ $di/dt=200A/us$
Body Diode Reverse Recovery Charge	Q_{rr}	-	110	-	nC	
Peak Reverse Recovery Current	I_{RRM}	-	4.2	-	A	

Typical Performance Characteristics

Fig 1: Output Characteristics $T_j=25^{\circ}\text{C}$

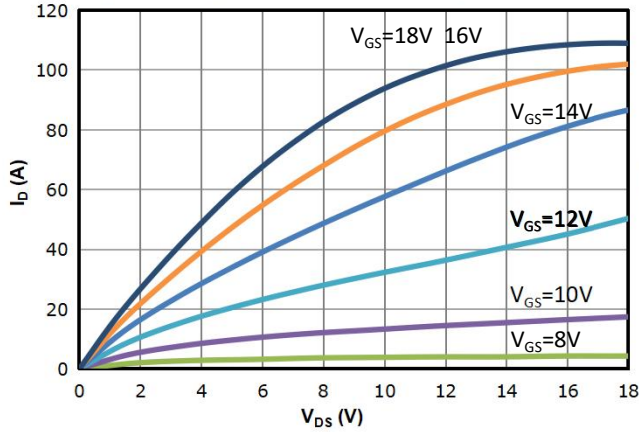


Fig 2: Output Characteristics $T_j=175^{\circ}\text{C}$

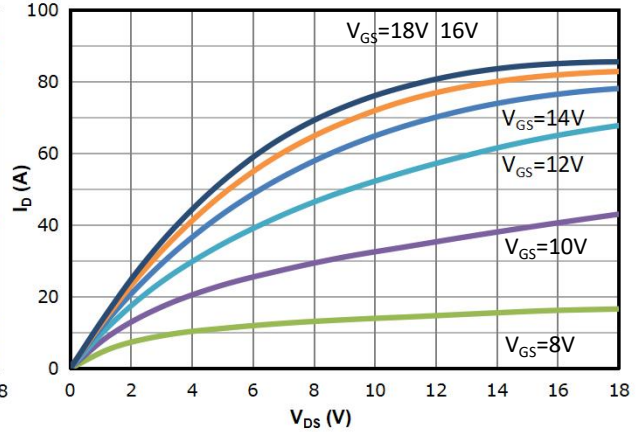


Fig 3: Transfer Characteristics

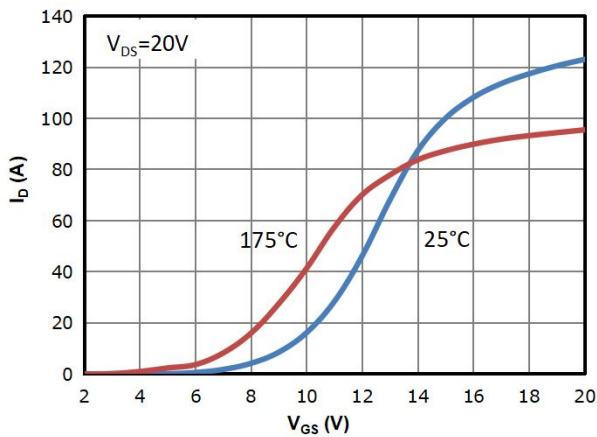


Fig 4: $R_{ds(on)}$ vs Drain Current and Gate Voltage

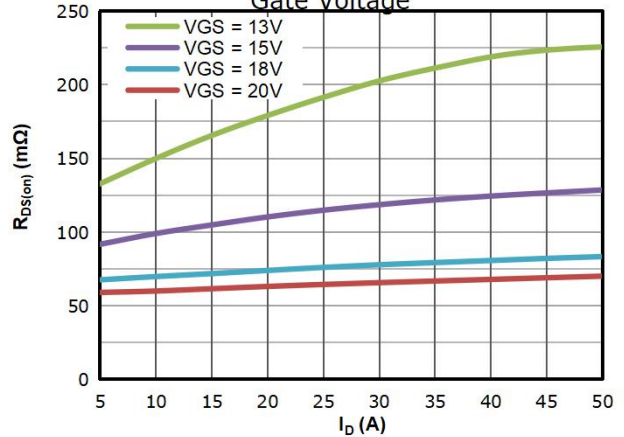


Fig 5: $R_{ds(on)}$ vs. Temperature

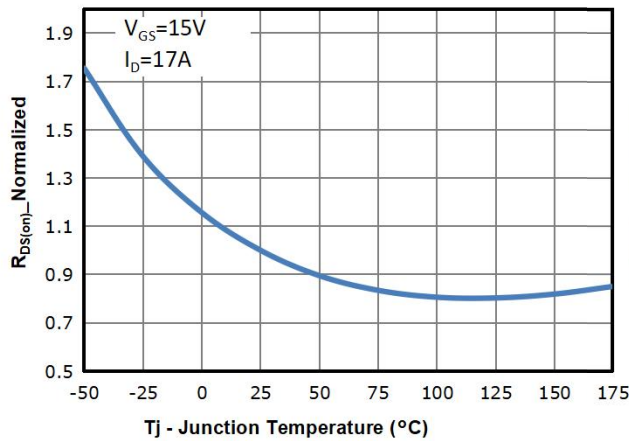


Fig 6: $V_{gs(th)}$ vs. Temperature

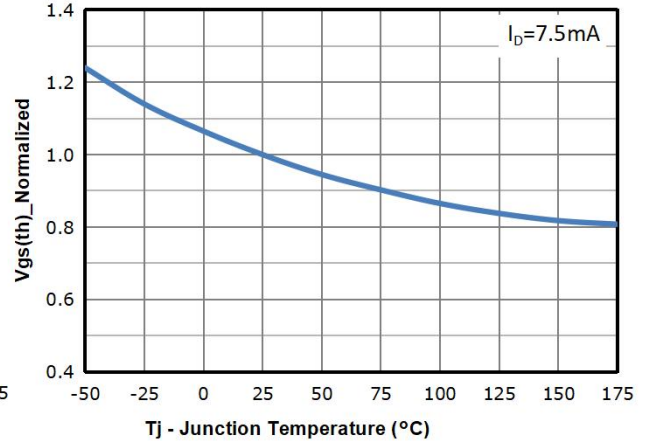


Fig 7: BVdss vs. Temperature

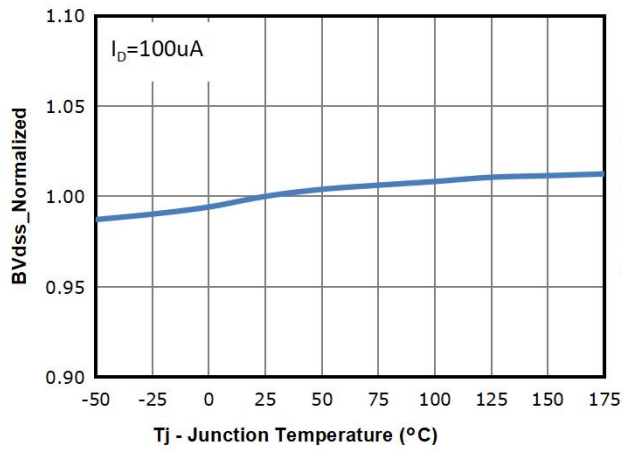


Fig 8: Capacitance Characteristics

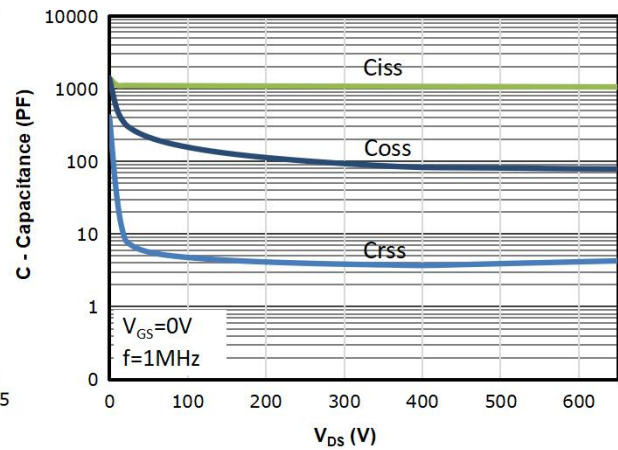


Fig 9: Gate Charge Characteristics

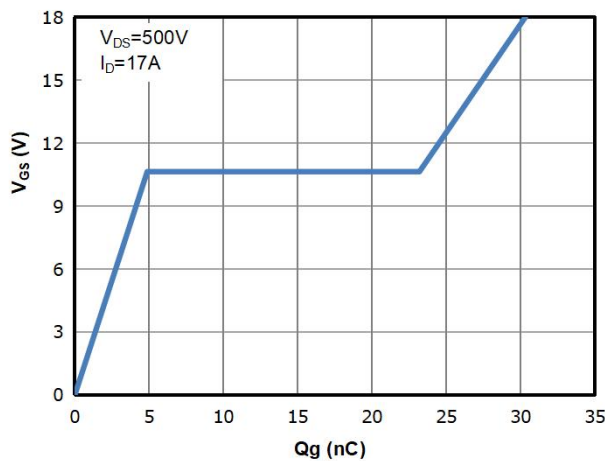


Fig 10: Body-diode Forward Characteristics

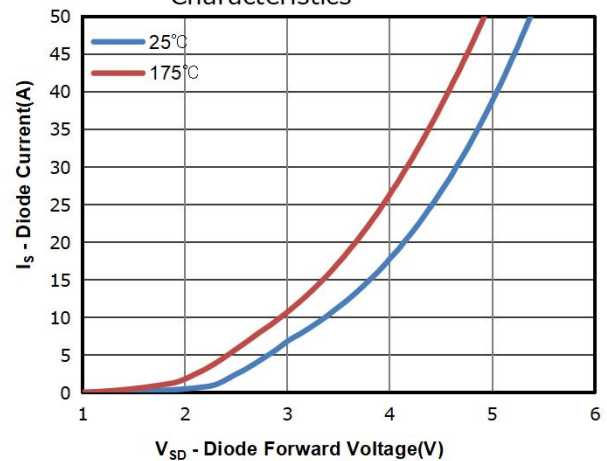


Fig 11: Power Dissipation

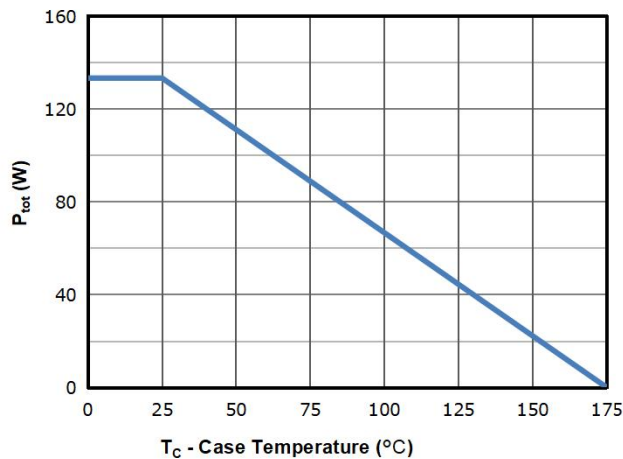


Fig 12: Drain Current Derating

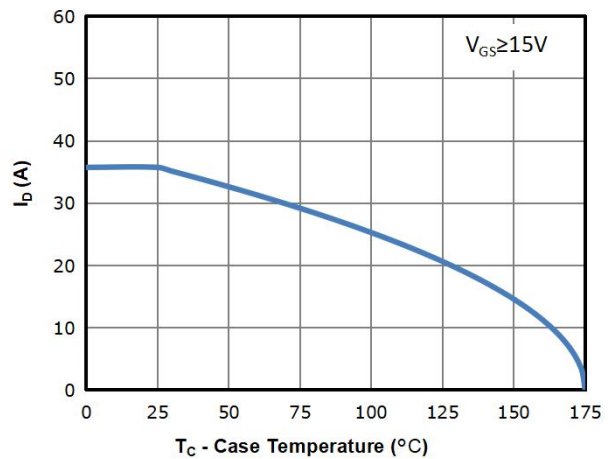




Fig 13: Safe Operating Area

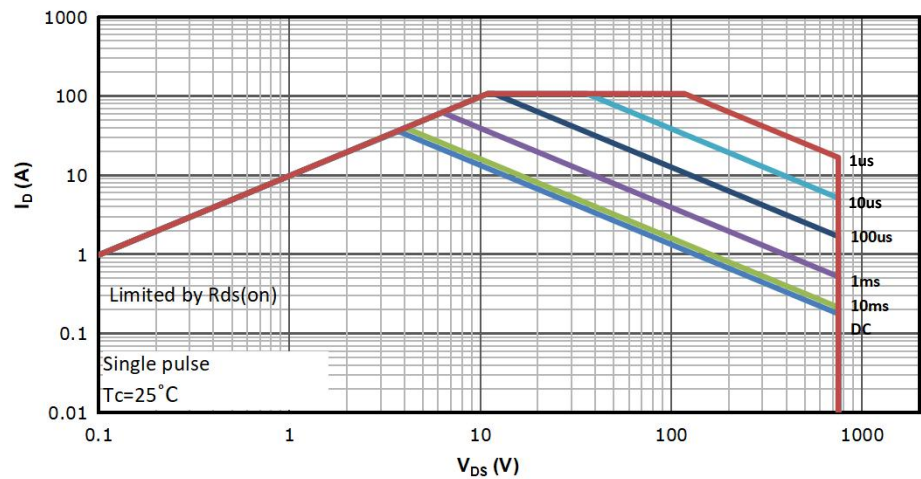
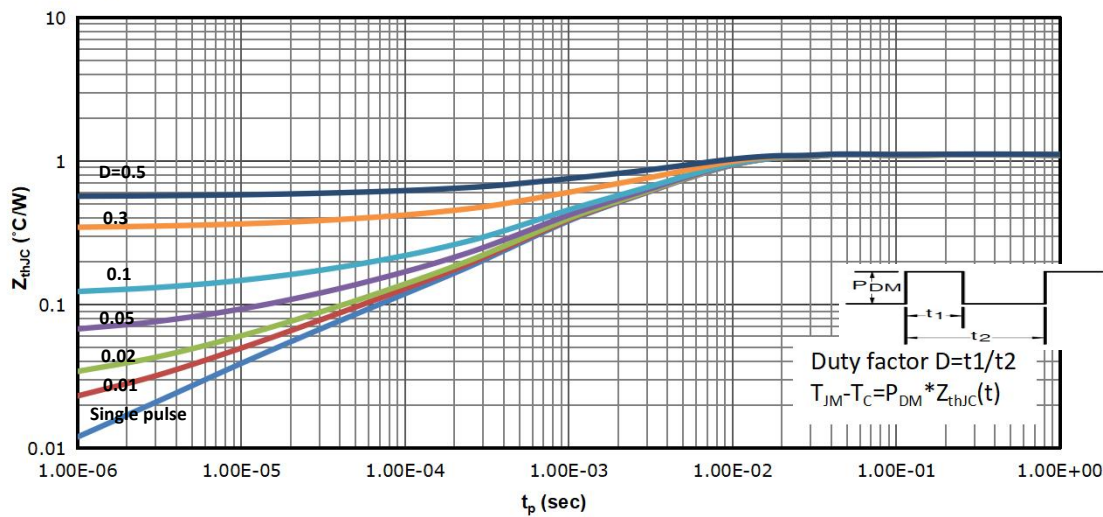
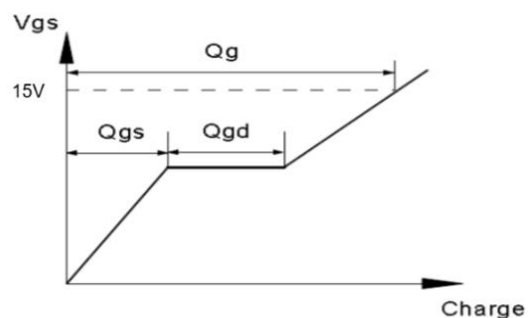
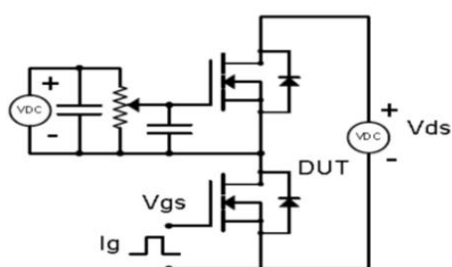


Fig 14: Max. Transient Thermal Impedance

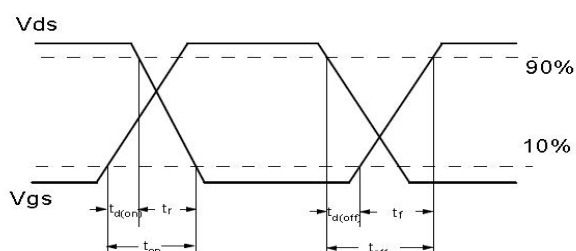
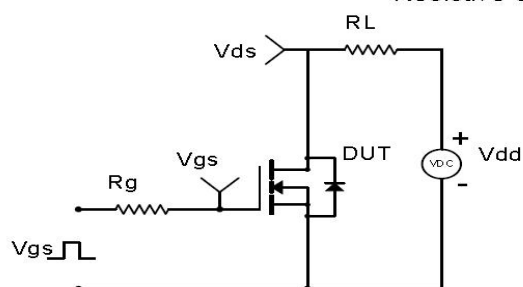


Test Circuit & Waveform

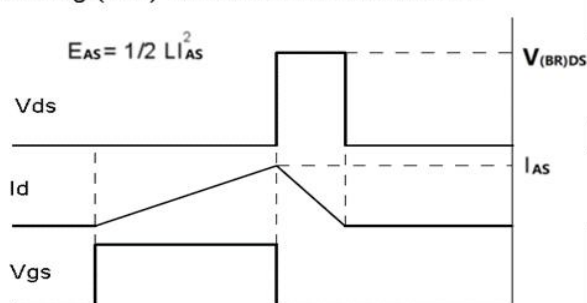
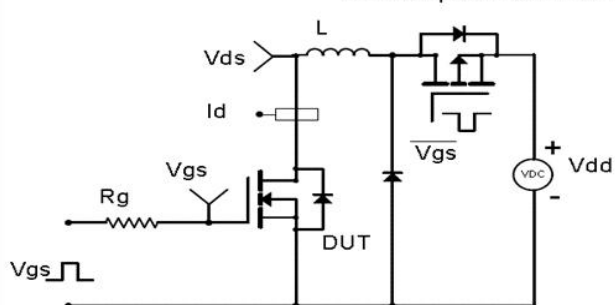
Gate Charge Test Circuit & Waveform



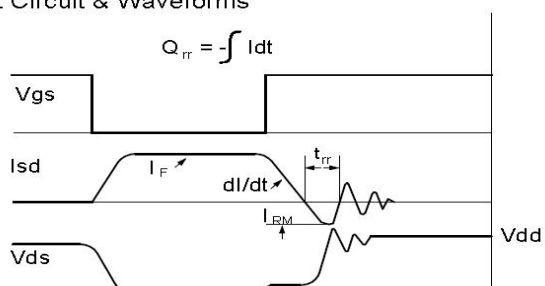
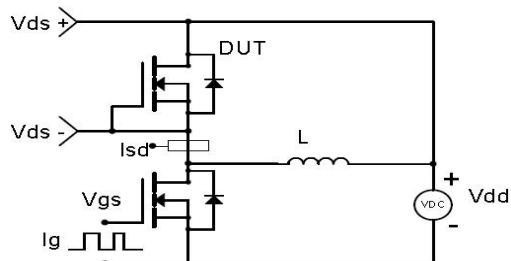
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



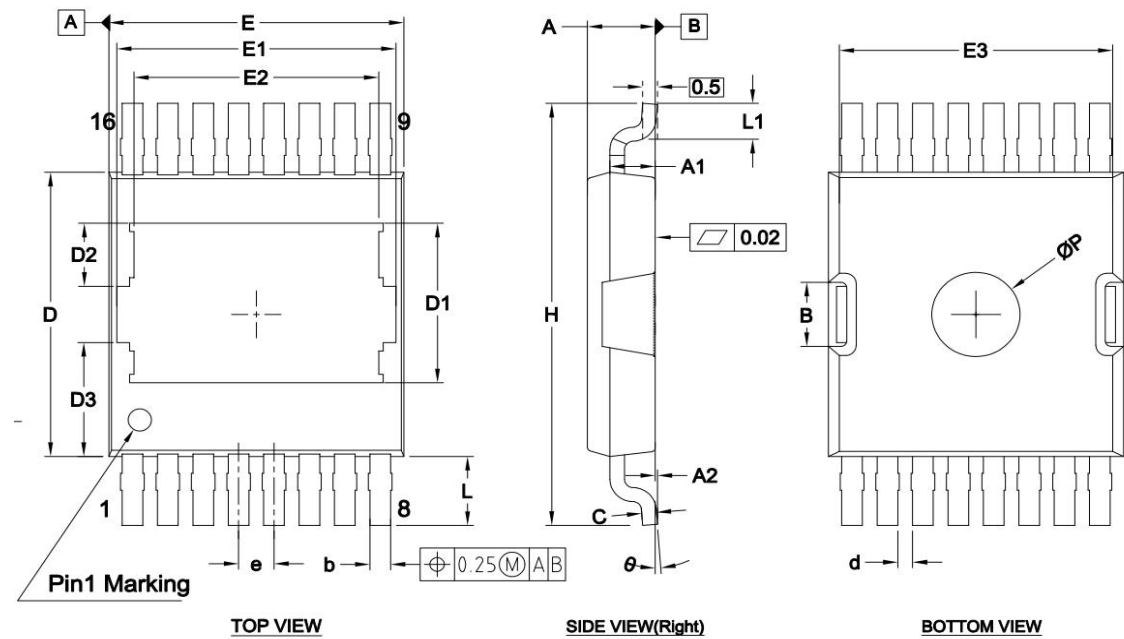
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Package Outline: TOLT



DIM	MIN.	NOM.	MAX.
A	2.25	2.30	2.35
A1	1.44	1.54	1.64
A2	0.01	---	0.16
b	0.60	0.70	0.80
c	0.40	0.50	0.60
d	0.40	0.50	0.60
e	1.20 BSC		
D	10.00	10.10	10.30
D1	5.47	5.67	5.87
D2	2.04	2.24	2.44
D3	4.05 REF.		
E	9.70	10.00	10.30
E1	9.46 REF.		
E2	8.10	8.30	8.50
E3	9.07	9.27	9.47
H	14.80	15.00	15.20
L	2.25	2.45	2.65
L1	1.35	1.50	1.65
ØP	2.35	2.50	2.65
B	2.08	2.28	2.48
θ	1°	3°	5°

Disclaimer

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

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