

Features

- Uses PingWei advanced PerfectMOS3 technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent $Q_g \times R_{DS(on)}$ product(FOM)
- Qualified according to AEC-Q101 criteria

Benefits

- High robustness and reliability
- Increases maximum current capability
- Low power loss, high power density

Applications

- General automotive applications
- Motor Drivers
- DC-DC

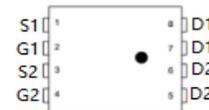
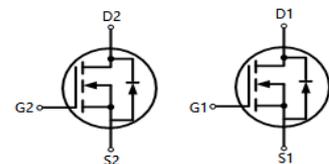
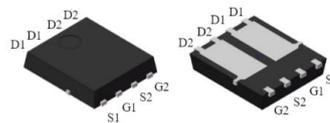


100% DVDS Tested
100% Avalanche Tested

Product Summary

V_{DS}	40V
$R_{DS(on)}@10V$ typ	4.8mΩ
ID (Silicon limit)	85A

DFN5x6双基岛



Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
PWC058N04ES3LDQ	C058N04ES3LDQ	DFN5x6双基岛	Tape&Reel	13 inches	12mm	5000pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit	
Drain-source voltage	V_{DS}	40	V	
Continuous drain current	I_D	85	A	
$T_C = 25^\circ\text{C}$ (Silicon limit)		40		
$T_C = 25^\circ\text{C}$ (Package limit)		60		
$T_C = 100^\circ\text{C}$ (Silicon limit)		18		
$T_a = 25^\circ\text{C}$ (See RthJA)				
Pulsed drain current ($T_C = 25^\circ\text{C}$, $tp=100\mu\text{s}$)	$I_{D \text{ pulse}}$	159	A	
Avalanche energy, single pulse ($L=0.1\text{mH}$, $V_{ds}=32\text{V}$)	E_{AS}	19	mJ	
Gate-Source voltage	V_{GS}	± 20	V	
Power dissipation	P_{tot}	70	W	
$T_C = 25^\circ\text{C}$				3.0
$T_a = 25^\circ\text{C}$ (See RthJA)				
Operating junction and storage temperature	T_j, T_{stg}	-55...+175	$^\circ\text{C}$	
Reflow soldering temperature (10s)	T_{sold}	260	$^\circ\text{C}$	

Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction - case.	RthJC	-	-	2.2	°C/W	-
Thermal resistance, junction - ambient	RthJA	-	50	-	°C/W	Note 1

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV _{DSS}	40	-	-	V	V _{GS} =0V, I _D =250uA
Gate threshold voltage	V _{GS(th)}	1.2	-	2.5	V	V _{DS} =V _{GS} , I _D =250uA
Zero gate voltage drain current	I _{DSS}	-	0.01	1	μA	V _{DS} =40V, V _{GS} =0V T _j =25°C T _j =150°C
Gate-source leakage current	I _{GSS}	-	±10	±100	nA	V _{GS} =±20V, V _{DS} =0V
Drain-source on-state resistance	R _{DS(on)}	-	4.8	5.8	mΩ	V _{GS} =10V, I _D =20A
		-	7.8	11.7		V _{GS} =4.5V, I _D =10A
Transconductance	g _{fs}	-	25	-	S	V _{DS} =5V, I _D =20A

Dynamic Characteristic

Input Capacitance	C _{iss}	-	716	-	pF	V _{GS} =0V, V _{DS} =20V, f=1MHz
Output Capacitance	C _{oss}	-	301	-		
Reverse Transfer Capacitance	C _{rss}	-	18	-		
Gate Total Charge	Q _G	-	13	-	nC	V _{DS} =20V, I _D =20A , V _{GS} =10V
Gate-Source charge	Q _{gs}	-	3	-		
Gate-Drain charge	Q _{gd}	-	2	-		
Turn-on delay time	t _{d(on)}	-	5	-	ns	V _{GS} =10V, V _{DD} =20V, R _G =10Ω, RL=1.6Ω
Rise time	t _r	-	6	-		
Turn-off delay time	t _{d(off)}	-	15	-		
Fall time	t _f	-	3	-		
Gate resistance	R _G	-	2.6	-	Ω	V _{GS} =0V, V _{DS} =0V, f=1MHz



Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	-	1.2	V	$V_{GS}=0V, I_{SD}=20A$
Body Diode Continuous Forward Current	I_S	-	-	40	A	TC = 25°C
Body Diode Pulsed Current	I_S pulse	-	-	159	A	TC = 25°C
Body Diode Reverse Recovery Time	t_{rr}	-	27.6	-	ns	$V_D=20V, I_F=20A,$ $dI/dt=100A/\mu s$
Body Diode Reverse Recovery Charge	Q_{rr}	-	12.8	-	nC	

Note1 : 20mm x 20mm x 1.6mm, 1oz single Copper FR-4.

Typical Performance Characteristics

Fig 1: Output Characteristics

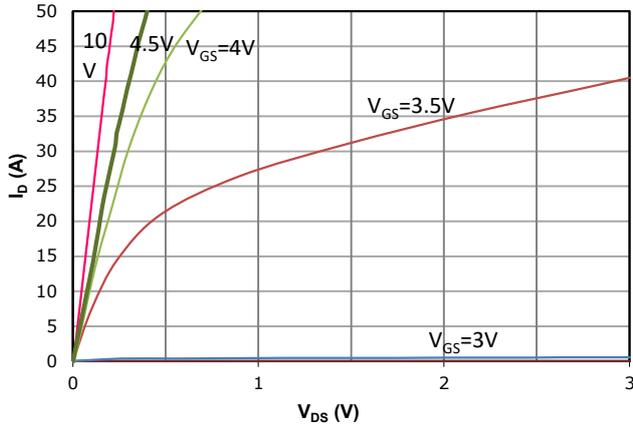


Fig 2: Transfer Characteristics

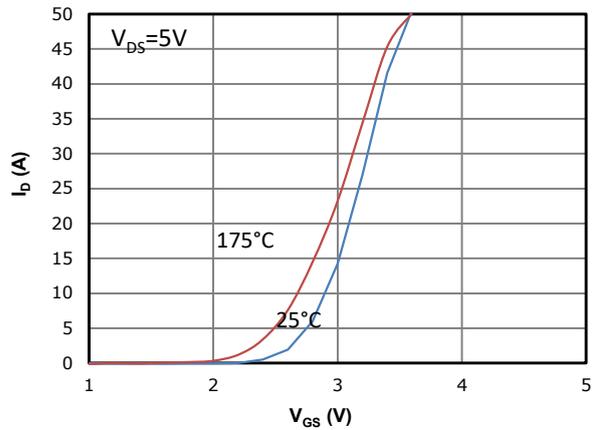


Fig 3: $R_{DS(on)}$ vs Drain Current and Gate Voltage

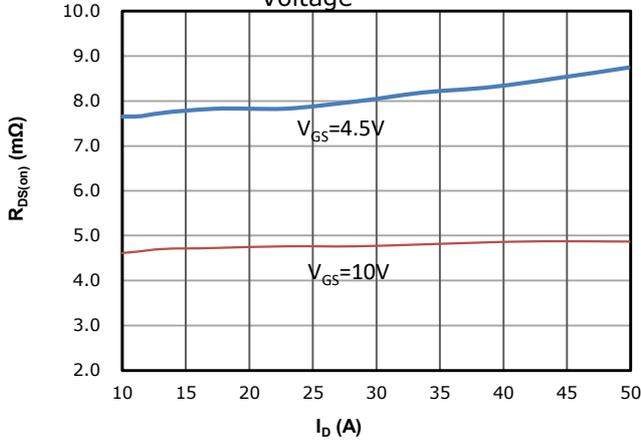


Fig 4: $R_{DS(on)}$ vs Gate Voltage

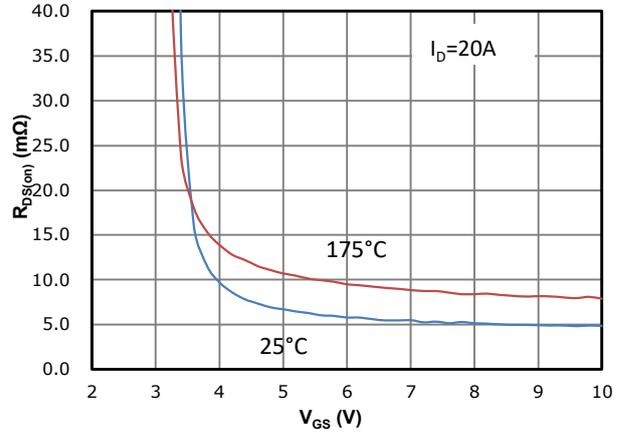


Fig 5: $R_{DS(on)}$ vs. Temperature

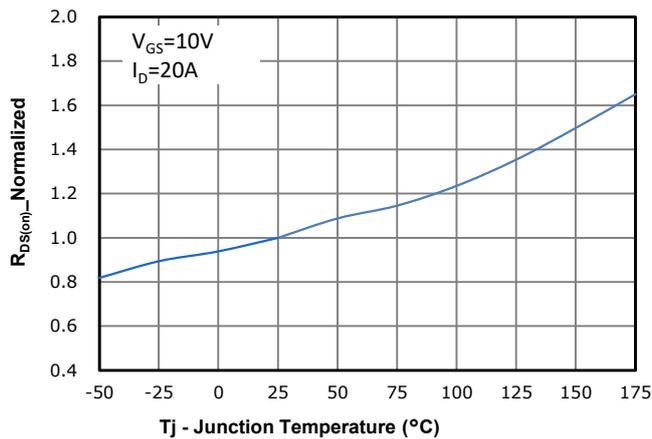


Fig 6: $V_{GS(th)}$ vs. Temperature

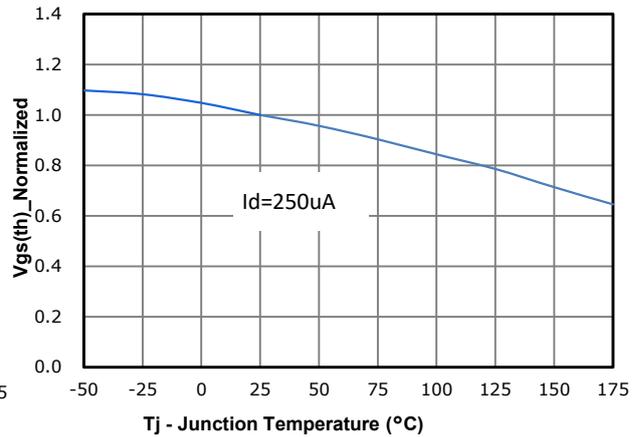


Fig 7: BVdss vs. Temperature

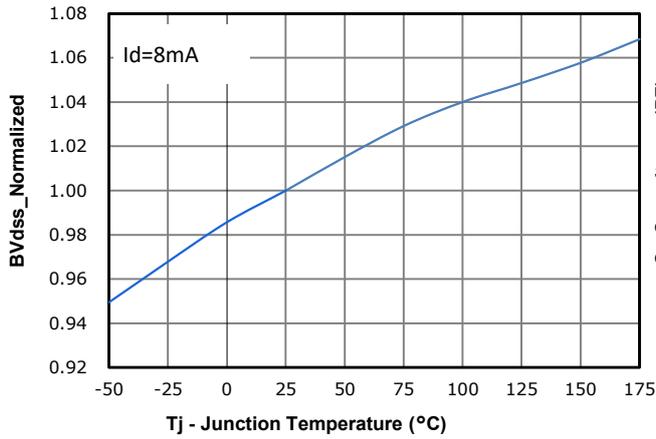


Fig 8: Capacitance Characteristics

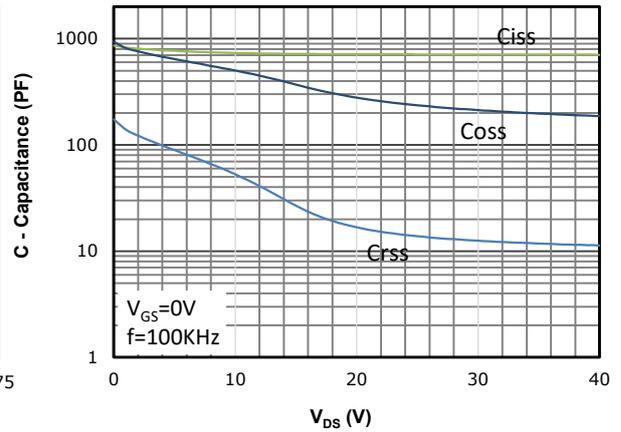


Fig 9: Gate Charge Characteristics

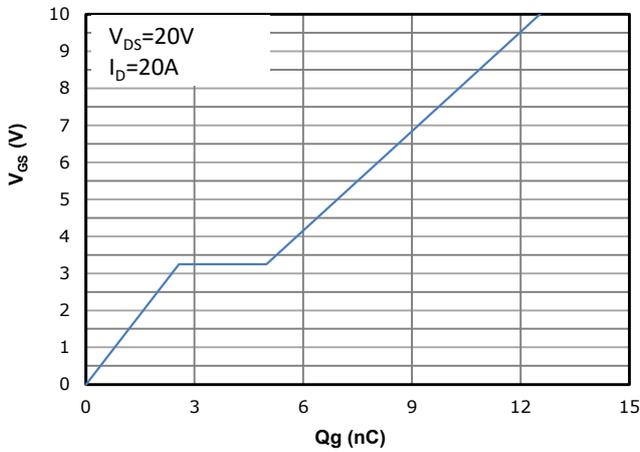


Fig 10: Body-diode Forward Characteristics

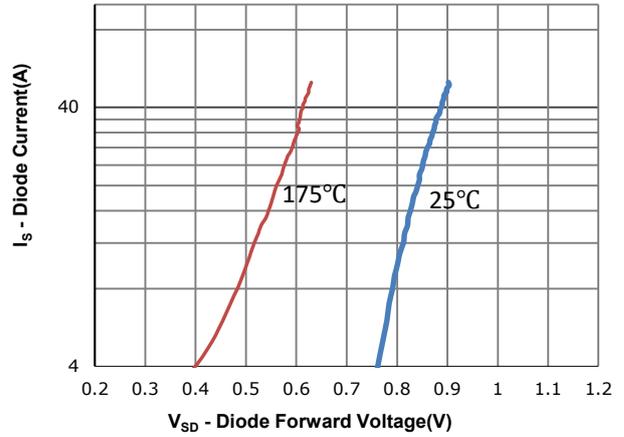


Fig 11: Power Dissipation

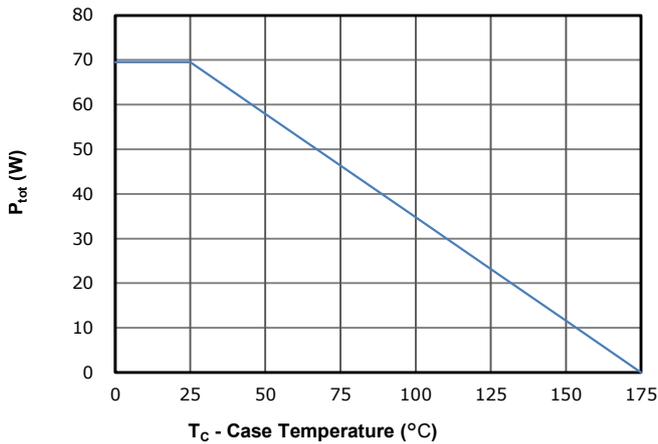


Fig 12: Drain Current Derating

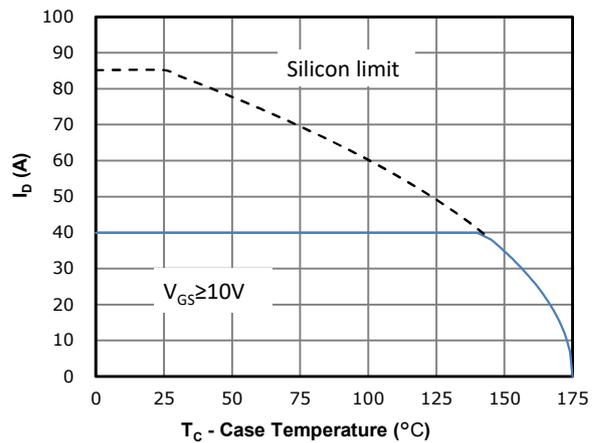


Fig 13: Safe Operating Area

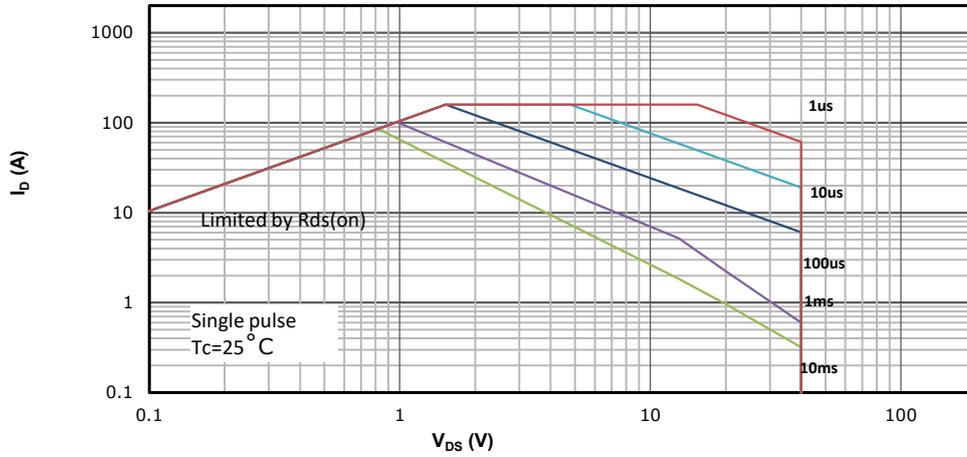
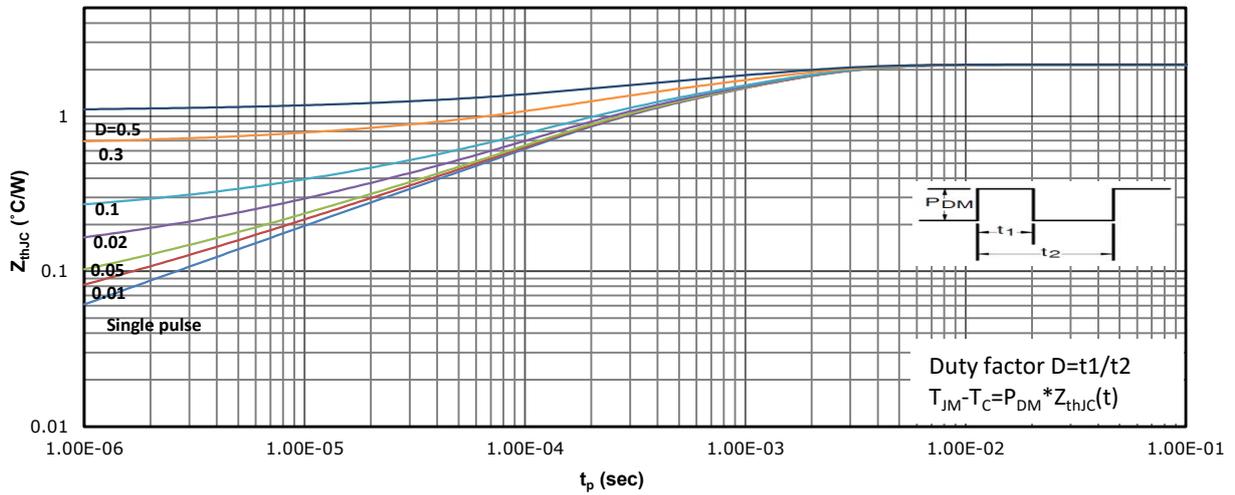
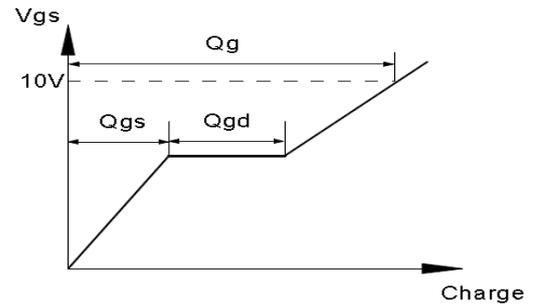
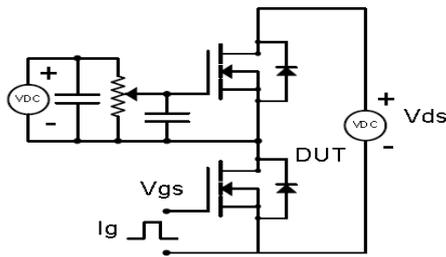


Fig 14: Max. Transient Thermal Impedance

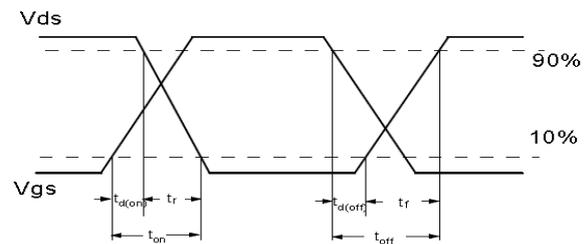
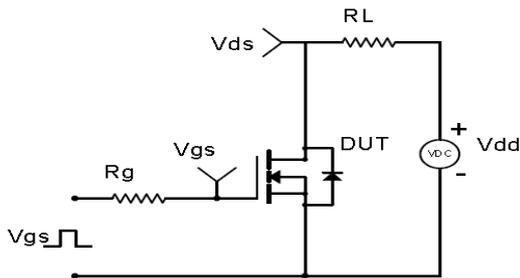


Test Circuit & Waveform

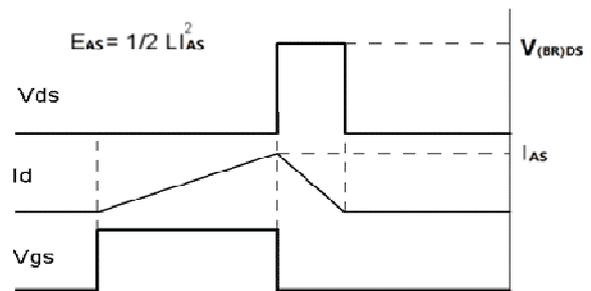
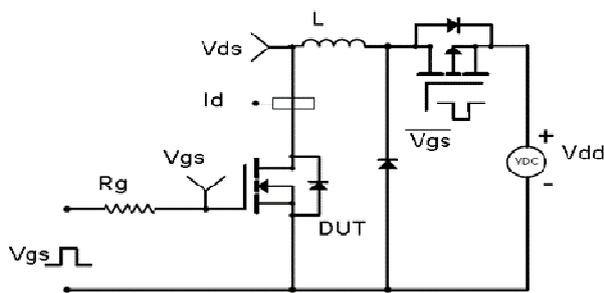
Gate Charge Test Circuit & Waveform



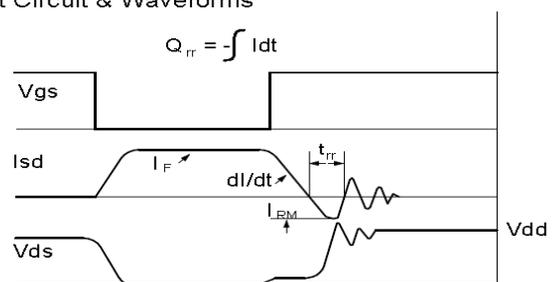
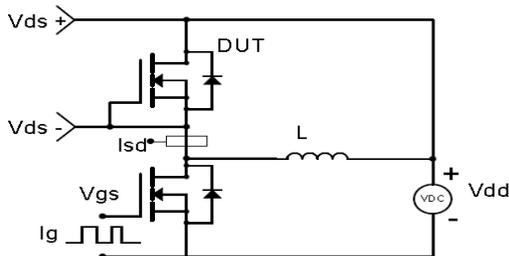
Resistive Switching Test Circuit & Waveforms



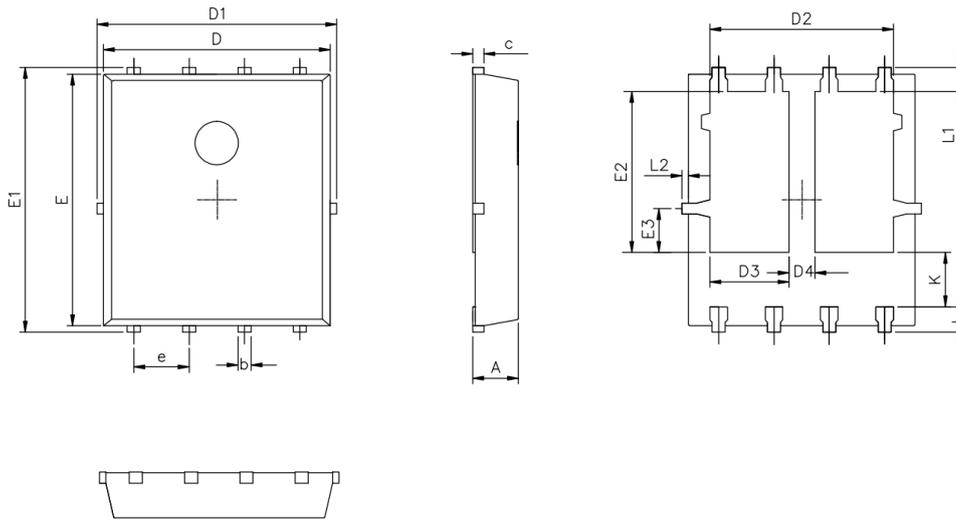
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline: DFN5x6 Double Base Island Package Outline



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.90	1.10	0.035	0.043
b	0.25	0.50	0.010	0.020
c	0.10	0.30	0.004	0.012
D	4.80	5.30	0.189	0.209
D1	4.90	5.50	0.193	0.217
D2	3.92	4.20	0.154	0.165
D3	1.61	1.81	0.063	0.071
D4	0.50	0.70	0.020	0.028
E	5.65	5.85	0.222	0.230
E1	5.90	6.20	0.232	0.244
E2	3.33	3.78	0.131	0.149
E3	0.80	1.00	0.031	0.039
e	1.27		0.050	
L	0.40	0.70	0.016	0.028
L1	0.65		0.026	
L2	0.00	0.15	0.000	0.006
K	1.00	1.50	0.039	0.059



Revision History

Revision	Date	Major changes
1.0	2025/10/21	Release of Normal Version.
1.1	2026/3/5	Update EAS from 7mJ to 19mJ.

Disclaimer

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

The product is not intended for use in applications that require extraordinary levels of quality and reliability, such as aviation/aerospace and life-support devices or systems.

Buyer is responsible for its products and applications using PingWei products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by PingWei.

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