

Features

- Uses PingWei advanced PerfectMOS2 technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent $Q_g \times R_{DS(on)}$ product(FOM)
- Qualified according to AEC-Q101 criteria

Benefits

- High robustness and reliability
- Increases maximum current capability
- Low power loss, high power density
- Solid design of source leadframe

Applications

- General automotive applications
- 12V or 24V Power control unit

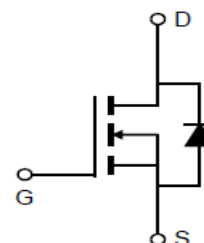
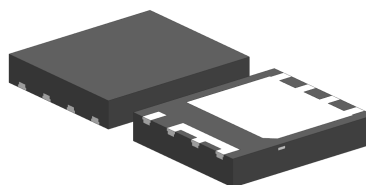


100% DVDS Tested
100% Avalanche Tested

Product Summary

V_{DS}	60V
$R_{DS(on)}@10V$ typ	0.85mΩ
I_D (Silicon limit)	314A

Super DFN5*6

**Package Marking and Ordering Information**

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
PWC010N06SESQ	C010N06SESQ	Super DFN5*6	Tape&Reel	13 inches	12mm	5000pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	60	V
Continuous drain current $T_C = 25^\circ\text{C}$ (Silicon limit) $T_C = 25^\circ\text{C}$ (Package limit) $T_C = 100^\circ\text{C}$ (Silicon limit)	I_D	314 150 222	A
Pulsed drain current ($T_C = 25^\circ\text{C}$, $t_p=100\mu\text{s}$)	$I_{D \text{ pulse}}$	1185	A
Avalanche energy, single pulse ($L=0.1\text{mH}$)	E_{AS}	504	mJ
Gate-Source voltage	V_{GS}	± 20	V
Power dissipation $T_C = 25^\circ\text{C}$	P_{tot}	217	W
Operating junction and storage temperature	T_j, T_{stg}	-55...+175	$^\circ\text{C}$
Reflow soldering temperature (10s)	T_{sold}	260	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – case.	RthJC	-	0.47	0.7	°C/W	-
Thermal resistance, junction - ambient ¹⁾	RthJA	-	48	-	°C/W	40x40x1.6mm Single 2oz Copper FR-4 PCB.

Electrical Characteristic (at T_j = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV _{DSS}	60	-	-	V	V _{GS} =0V, I _D =250uA
Gate threshold voltage	V _{GS(th)}	2.4	3	3.6	V	V _{DS} =V _{GS} , I _D =250uA
Zero gate voltage drain current	I _{DSS}	-	-	1 100	μA	V _{DS} =60V, V _{GS} =0V T _j =25°C T _j =125°C
Gate-source leakage current	I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
Drain-source on-state resistance	R _{DS(on)}	-	0.85	1.0	mΩ	V _{GS} =10V, I _D =50A
Transconductance	g _{fs}	-	140	-	S	V _{DS} =5V, I _D =50A

Dynamic Characteristic

Input Capacitance	C _{iss}	-	9365	13111	pF	V _{GS} =0V, V _{DS} =30V, f=100kHz
Output Capacitance	C _{oss}	-	2250	3375		
Reverse Transfer Capacitance	C _{rss}	-	52	104		
Gate Total Charge	Q _G	-	122	-	nC	V _{DS} =30V, I _D =30A V _{GS} =10V
Gate-Source charge	Q _{GS}	-	41	-		
Gate-Drain charge	Q _{GD}	-	14	-		
Turn-on delay time	t _{d(on)}	-	29	-	ns	V _{GS} =10V, V _{DD} =30V, R _{G_ext} =1.6Ω, I _D =30A
Rise time	t _r	-	24	-		
Turn-off delay time	t _{d(off)}	-	63	-		
Fall time	t _f	-	11	-		
Gate resistance	R _G	-	0.7	-	Ω	V _{GS} =0V, V _{DS} =0V, f=1MHz

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	-	1.2	V	$V_{GS}=0V, I_{SD}=50A$
Body Diode Continuous Forward Current	I_S	-	-	150	A	TC = 25°C
Body Diode Pulsed Current	I_S pulse	-	-	1185	A	TC = 25°C, $t_p=100\mu s$
Body Diode Reverse Recovery Time	t_{rr}	-	83	-	ns	$I_F=30A,$ $dI/dt=100A/\mu s$
Body Diode Reverse Recovery Charge	Q_{rr}	-	75	-	nC	

Typical Performance Characteristics

Fig 1: Output Characteristics

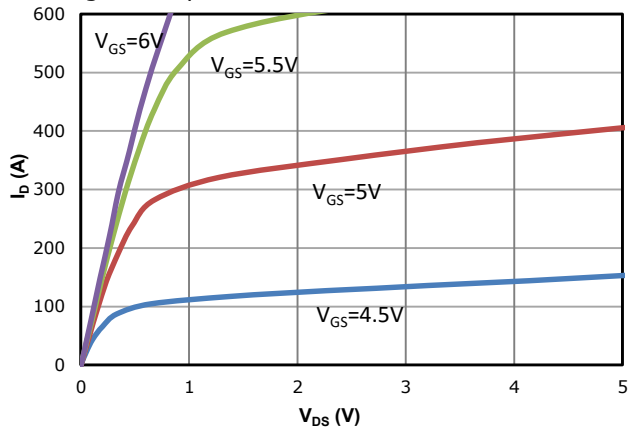


Fig 2: Transfer Characteristics

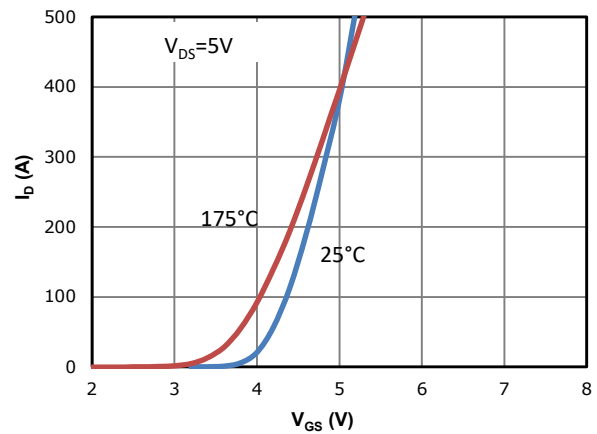


Fig 3: $R_{DS(on)}$ vs Drain Current and Gate Voltage

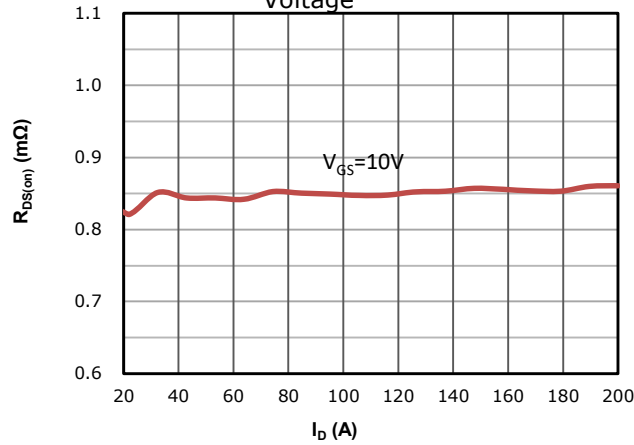


Fig 4: $R_{DS(on)}$ vs Gate Voltage

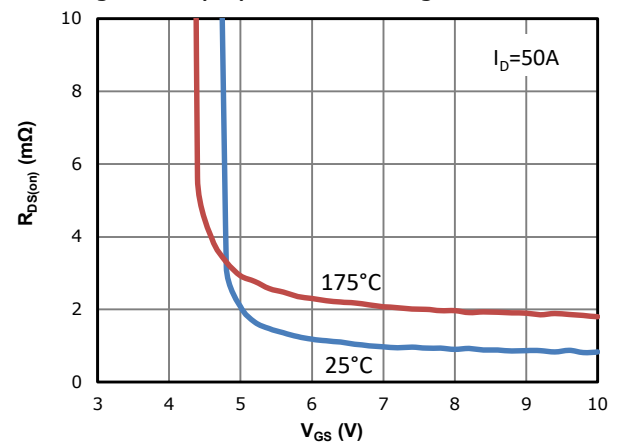


Fig 5: $R_{DS(on)}$ vs. Temperature

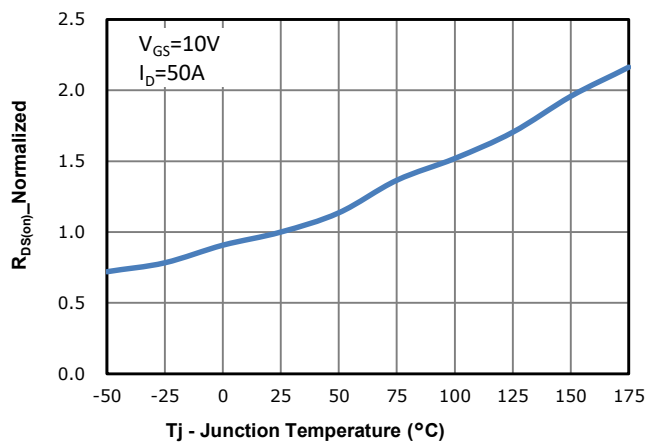


Fig 6: $V_{GS(th)}$ vs. Temperature

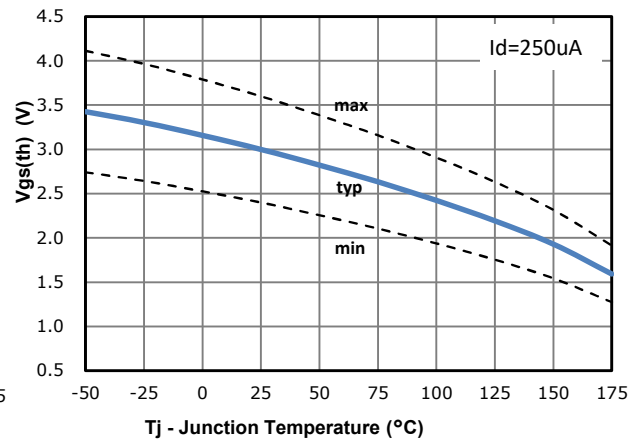


Fig 7: BVdss vs. Temperature

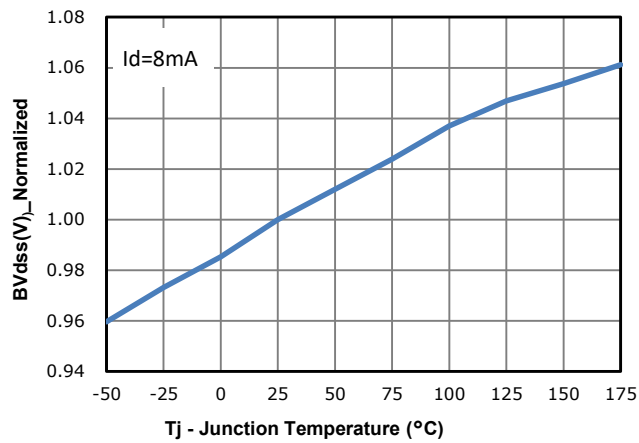


Fig 8: Capacitance Characteristics

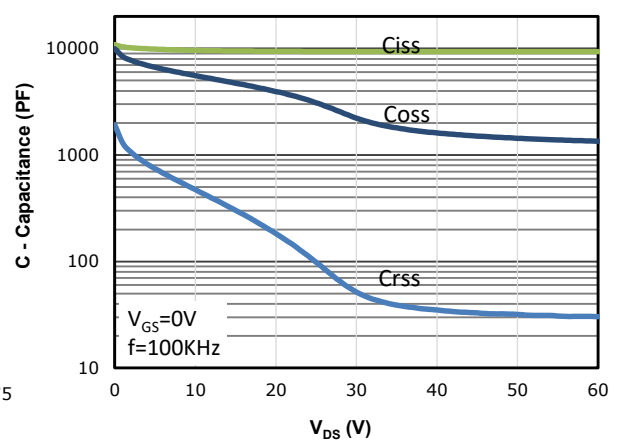


Fig 9: Gate Charge Characteristics

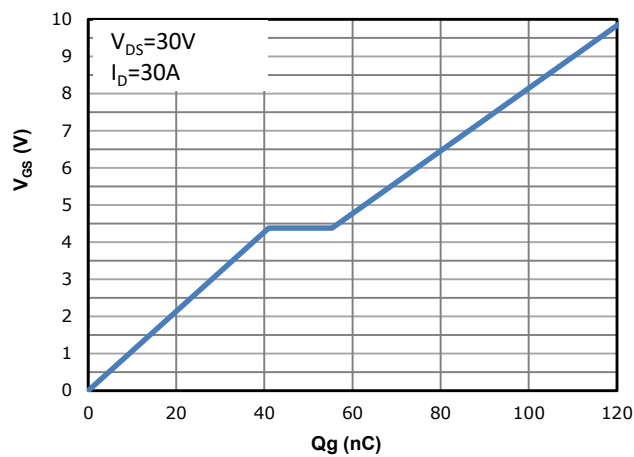


Fig 10: Body-diode Forward Characteristics

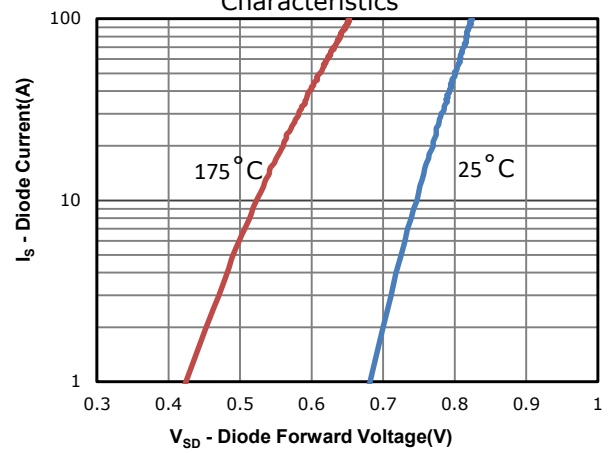


Fig 11: Power Dissipation

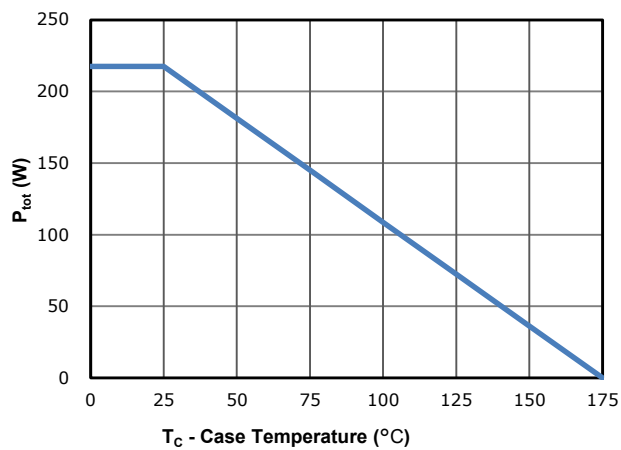


Fig 12: Drain Current Derating

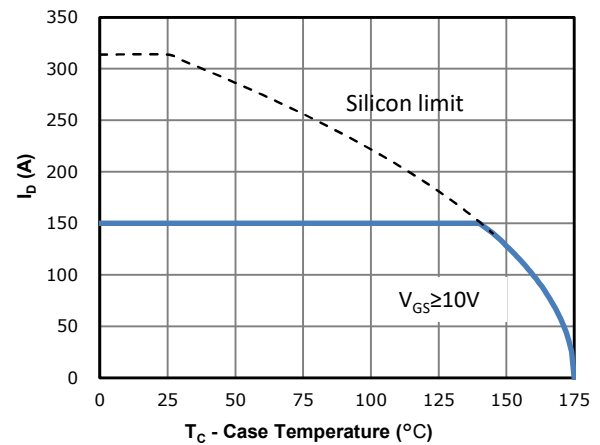


Fig 13: Safe Operating Area

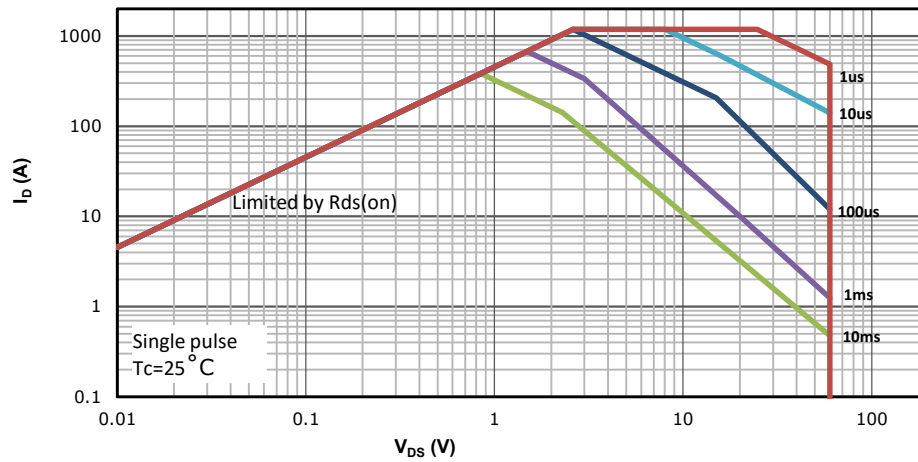
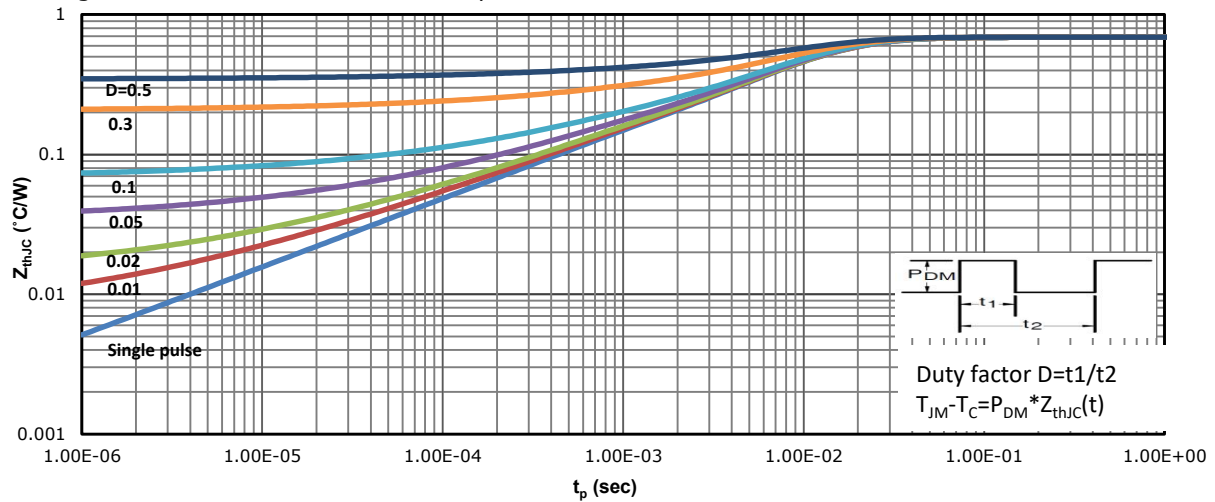
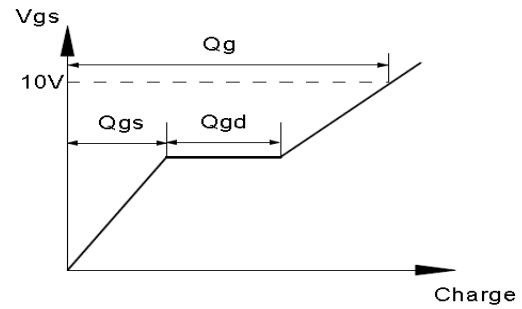
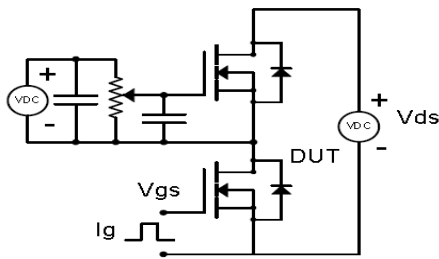


Fig 14: Max. Transient Thermal Impedance

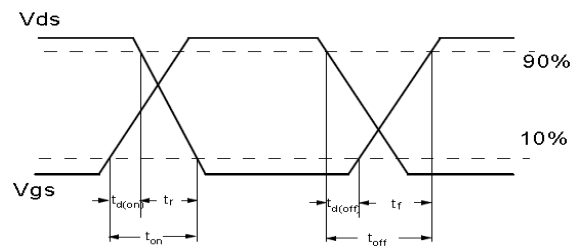
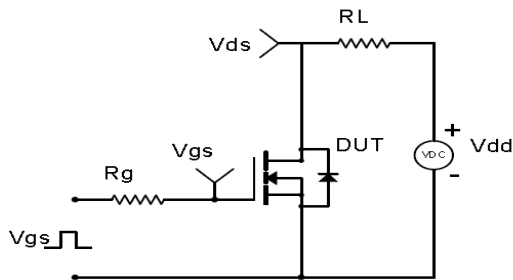


Test Circuit & Waveform

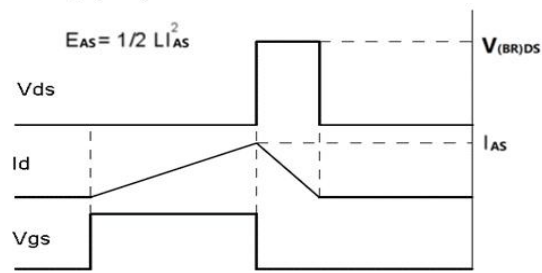
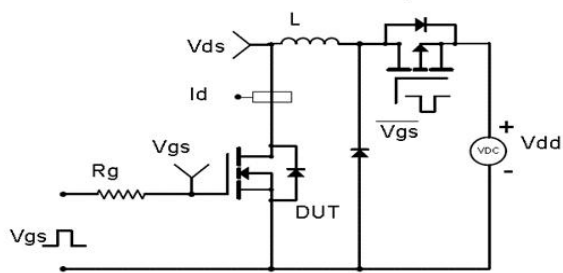
Gate Charge Test Circuit & Waveform



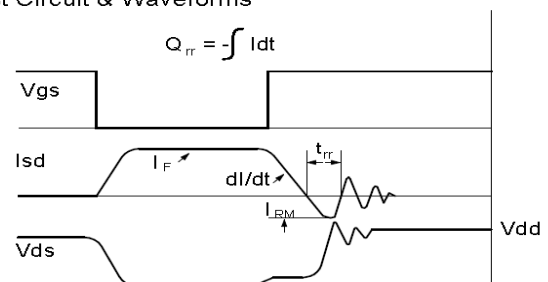
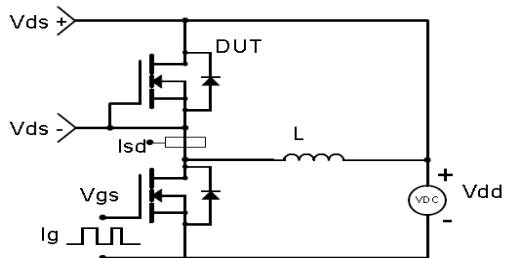
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

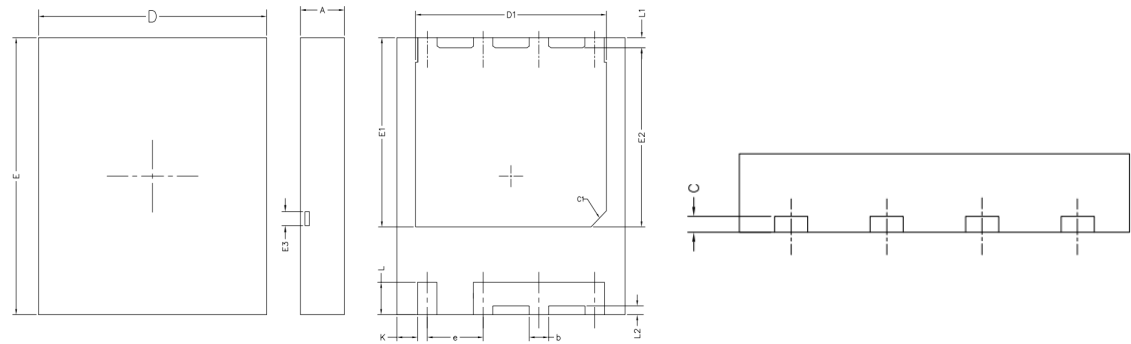


Diode Recovery Test Circuit & Waveforms





Package Outline: Super DFN5*6



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.90	1.10	0.035	0.043
b	0.34	0.54	0.013	0.021
c	0.20		0.20	
c1	0.25	0.45	0.010	0.018
D	5.10	5.30	0.201	0.209
D1	4.35	4.55	0.171	0.179
E	5.90	6.10	0.232	0.240
E1	4.00	4.10	0.157	0.161
E2	3.78	3.98	0.149	0.157
E3	0.20	0.40	0.008	0.016
e	1.28		0.050	
K	0.37	0.57	0.015	0.022
L	0.60	0.80	0.024	0.031
L1	0.12	0.32	0.005	0.013
L2	0.09	0.29	0.004	0.011

Revision History

Revision	Date	Major changes
1.0	2025/11/14	Release of Formal Version.

Disclaimer

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

The product is not intended for use in applications that require extraordinary levels of quality and reliability, such as aviation/aerospace and life-support devices or systems.

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