SB8P45 8.0AMPS. SCHOTTKY BARRIER RECTIFIERS

FEATURE

- . High current capability
- . Low forward voltage drop
- . Low power loss, high efficiency
- . High surge capability
- . High temperature soldering guaranteed 260°C /1 0sec/0.375" lead length at 5 lbs tension

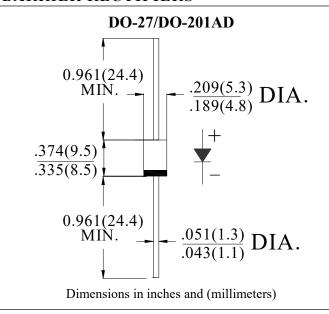
MECHANICAL DATA

. Terminal: Plated axial leads solderable per MIL-STD 202E, method 208C

. Case: Molded with UL-94 Class V-0 recognized Flame Retardant Epoxy

. Polarity: color band denotes cathode

. Mounting position: any



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at $25\,^{\circ}$ C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%

Type Number		SYM BOL	SB8P45	units	
Maximum Recurrent Peak Reverse Voltage		$V_{ m RRM}$	45	V	
Maximum RMS Voltage		$V_{ m RMS}$	31. 5	V	
Maximum DC blocking Voltage		$V_{ m DC}$	45	V	
Maximum Average Forward Rectified Current .375"(9.5mm) lead length		I _{F(AV)}	8.0	A	
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)		I_{FSM}	120	A	
Forward Voltage @T _J =25°C	At 8.0A DC	V _{F Max}	0.55 0.48	V	
	At 2.0A DC	$V_{ m FType}$ $V_{ m FType}$	0.40	v	
Maximum DC Reverse Current @T _J =25°C		7	0.2		
at rated DC blocking voltage	@ $T_J = 100$ °C	$I_{ m R}$	10	mA	
Typical Junction Capacitance (Note1)		$C_{ m J}$	160	pF	
Typical Thermal Resistance (Note2)		$R_{(\mathrm{JL})}$	35	°C/W	
		R _(JC)	15		
Storage Temperature		T _{STG}	-55 to +150	°C	
Operating Junction Temperature		$T_{ m J}$	-55 to +125	°C	

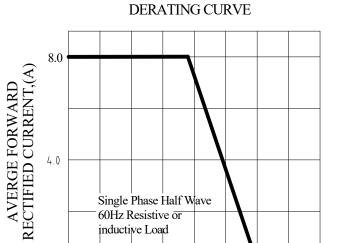
Note:

- 1. Measured at 1.0 MHz and applied reverse voltage of 4.0Vdc
- 2. Thermal Resistance from Junction to Ambient at 0.375"(9.5mm)lead length, vertical P.C. Board Mounted

0 0

RATING AND CHARACTERISTIC CURVES

FIG.1-TYPICAL FORWARD CURRENT



50

FIG.2-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

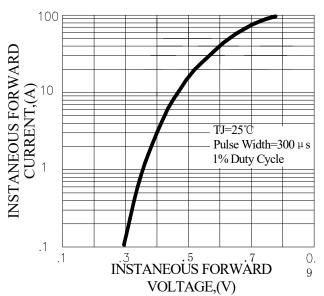


FIG.3-MAXIMUN NON-REPETITIVE FORWARD SURGE CURRENT

100

LEAD TEMPERTURE, (°C)

150

200

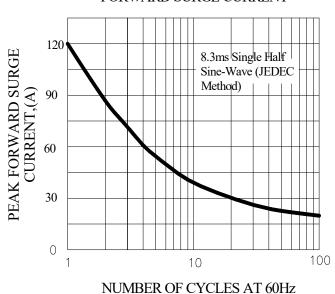
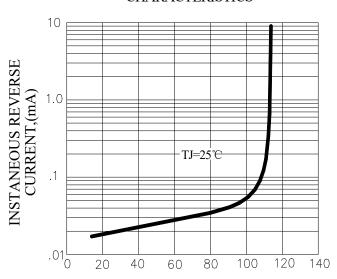


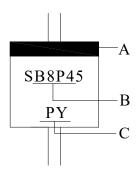
FIG.4-TYPICAL REVERSE CHARACTERISTICS



PERCENT OF RATED PEAK REVERSE VOLTAGE,(%)

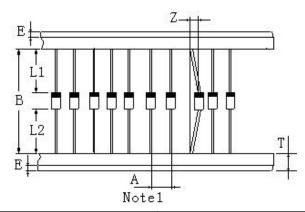
Marking and packaging illustration

1. Marking



SYMBOL	Explanation
A	Color Band Denotes Cathode
В	Product Name
C	Trademark

2. Packaging



ITEM	SYMBOL	SPECIFICATIONS	SPECIFICATIONS
I I ENI	STMBOL	(mm)	(inch)
Component alignment	Z	1.2max	0.048max
Tape width	T	6.0 ± 0.4	0.236 ± 0.016
Exposed adhesive	E	0.8max	0.032max
Body eccentricity	L1-L2	1.0max	0.040max
Component	A	10.0 ± 0.5	0.4 ± 0.02
Inner tap	В	52.0~53.5	2.05~2.11

NOTE:

Each component lead shall be sandwiched between tapes for a minimum of 2.5mm $\,(0.1 inch)\,$